Demonstration of a Portable HTS MMIC Microwave Receiver Front-end

Jia Du, Jia Wang, Ting Zhang, Dandan Bai, Yingjie Jay Guo, Fellow, IEEE, and Yusheng He

Abstract—We report the first demonstration of a portable HTS monolithic microwave integrated circuit (MMIC) receiver front-end module operating on a commercial mini cryocooler. The HTS circuit consists of a step-edge junction mixer and a number of HTS filters fabricated on a single MgO substrate. The HTS MMIC circuit is integrated with the mini cryocooler. The sample vacuum chamber, cold-head, compressor and cooling fans are all packed into one customer-designed portable box of approximately 350 mm × 350 mm × 250 mm in dimension. The HTS Josephson junction-based microwave circuit operated successfully in the cryocooler unshielded without observable performance degradation. The design and implementation of the compact unit and performance evaluation of a HTS MMIC frequency down-converter are presented.

Index Terms—High-temperature superconductor, Josephson mixer, receiver front-end, mini cryocooler.

I. INTRODUCTION

High temperature superconducting (HTS) thin-films have ultra-low surface resistance values at microwave frequencies, which have been applied to make high-Q resonators and “super filters” with superior performance [1–4]. Novel nonlinear high-frequency devices, such as oscillators and mixers [5–8], based on the low-noise Josephson junctions have been also developed and successfully integrated on-chip with HTS filters recently [9–11]. These microwave filters and Josephson junction devices are mainly being developed for wireless communication front-end receiver systems. Although significant progress has been made in recent years and some filters have been successfully deployed in commercial receiver front-end subsystems, the HTS receiver systems have not been widely adopted by industry due to their two drawbacks as below. The first one is the need of cooling either by cryogenics (liquid nitrogen or liquid helium) or electronic-powered cryocoolers. Implementation of superconducting electronic devices in HTS technology, instead of the low temperature superconductor (LTS) technology offers the potential to use much cheaper and more compact cooling systems. HTS filters have been successfully developed using advanced one-stage mini cryocoolers, but barely any demonstration of HTS Josephson junction-based active devices operating on these mini-cryocoolers due to their extreme sensitivity to mechanical vibration and electromagnetic interference (EMI) noise introduced by the mechanical coolers. Oppenlaender et al. successfully operated HTS array superconducting quantum interference filters (SQIFs) in a commercial miniature cryocooler [12] owing to a large dynamic range of these devices. The second drawback is that most HTS front-end receiver systems employ a superconductor-semiconductor hybrid configuration, i.e. using superconductor passive components combined with semiconductor active devices. This is because HTS Josephson junction-based active devices are far less developed than those of their LTS counterparts. This compromise makes the front-end bulky, inefficient and unable to exploit the full potential of the HTS superconductive components, results in the need of higher cooling power. A novel superconducting receiver system comprising both HTS passive and active devices is a much desirable technology. It can potentially improve the performance of present microwave receivers and reduce the cooling power required.

In this paper, we report the successful demonstration of a HTS monolithic microwave integrated circuit (MMIC) frequency down-converter integrated with a commercial mini cryocooler (LIHAN model TC4187B). The design and implementation of the compact unit and the performance evaluation of the HTS down-converter module are presented. To the best of our knowledge, this is the first demonstration of a portable HTS monolithic microwave integrated circuit receiver system operating on a commercial single-stage mini cryocooler.

II. HTS MMIC DEVICE DESCRIPTION

We have recently developed an all-HTS MMIC Josephson down-converter module [11] which consists of a step-edge Josephson junction mixer, a 10-12 GHz bandpass filter [3] for RF signal input, a lowpass filter [13] for the intermediate frequency (IF) output and a resonant strip line for local oscillator (LO) isolation. All the components are integrated onto a single 10 mm × 20 mm YBa2Cu3O7-δ (YBCO) film on MgO substrate. The HTS MMIC chip was fabricated using the CSIRO-developed step-edge junction technology [14, 15]. A packaged 10-12 GHz HTS MMIC down-converter chip is shown in Fig. 1. A PCB below the MMIC chip contains RF blocking network and DC biasing lines where 1 kΩ resistors
were inserted. The total size of the packaged MMIC down-converter module is approximately $25 \times 27 \times 15 \text{mm}^3$, which is very compact.

![YBCO step-edge Josephson junction](image)

Fig. 1. Photographs of a packaged 10-12 GHz HTS MMIC Josephson frequency down-converter; the inset shows the close view of the step-edge Josephson junction.

The DC characteristics and RF performance of an optimized HTS MMIC down-converter of the same design have been fully characterized at temperatures ranging from 20 to 80 K on a large two-stage pulse-tube cryocooler (PTC) where the compressor is located outside the laboratory. The detailed design considerations and the mixer performance evaluation of the HTS MMIC circuit can be found elsewhere [11]. In this work, we focused on the design and integration of the HTS device with a commercial single-stage mini cooler into a single box to form a portable HTS MMIC receiver sub-module. We have evaluated the performance of the HTS Josephson down-converter module to demonstrate the feasibility of a portable HTS MMIC microwave receiver front-end.

III. MINI CRYOCOOLER AND SYSTEM INTEGRATION

The commercial mini cryocooler (Model CT4187B) was supplied by LIHAN Thermacootechnologies Co. (www.lihantech.com), China. It is a modified split type pulse-tube cryocooler and has a cooling power of 10 W at 80 K. It reaches 70 K in about 30 minutes and the lowest attainable temperature is $\sim 55$ K after installation of all the cables and wires. The orientation of the split compressor and the cold finger was customer designed and implemented by LIHAN as shown in Fig. 2(a). It was designed in such a way that the anticipated magnetic flux generated by the compressor is parallel to the HTS chip surface so the step-edge junction would see minimum vertical flux. An instrument box was designed to accommodate all the parts including the compressor, cold finger, cooler power control circuits, fans and the sample vacuuming chamber as shown in Fig. 2 (b). Stainless steel plates were placed between the main parts to provide further isolation between the cold-head and the compressor and the power control unit. Fig. 2 (c) shows the customer-designed (built by LIHAN) vacuum chamber for housing the sample on the cold finger with vacuuming valve and the panels for cable and DC wire connections.

![Schematic diagram](image)

Fig. 2. Schematic diagrams of (a) LIHAN model CT4187B split-type mini cryocooler; (b) a housing box for all parts – compressor, cold finger, sample vacuuming chamber, cooler control circuit and fans; (c) sample vacuum chamber integrated with the coldfinger.

![Photographs](image)

Fig. 3 (a) shows the photographs of the mounted HTS MMIC Josephson down-converter on the cold finger inside the vacuum chamber. A Cu sample plate was used to thermally anchor three coaxial cables to maintain temperature stability (reduce the thermal exchange via the coaxial cables). Semi-rigid stainless steel coaxial cables were used inside the chamber and Cu coaxial cables were used outside the chamber. Fig. 3 (b) shows the enclosed box of the HTS MMIC circuit integrated with the mini cryocooler, which has a dimension of approximately $350 \text{mm} \times 350 \text{mm} \times 250 \text{mm}$. This is a portable all-HTS microwave integrated receiver sub-system.

IV. EXPERIMENTAL RESULTS OF THE PORTABLE HTS MMIC DOWN-CONVERTER

The performance of one HTS MMIC down-converter integrated with the mini cryocooler was evaluated and the results at 55 K are presented here. The step-edge junction in this down-converter has an $R_n$ value of $\sim 25 \Omega$ and unsuppressed $I_c = 15 \mu A$ at 55 K (note that the $I_c$, $R_n$ values are different from the mixer reported in [11]). In this paper, we will not discuss in details the DC characteristics of the step-edge junction and their influence on the mixer performance such as the suppression of LO power $P_{1LO}$ on the junction $I_c$ (i.e. $\partial I_c/\partial P_{1LO}^{1/2}$) and the mixer conversion efficiency. More details of such discussion can be found in a recent paper [11].

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A few measurement results of the HTS module integrated in the mini-cooler are presented here to demonstrate a working portable HTS receiver front-end. The cable losses have not been deducted from all the power levels shown in the graphs.

Fig. 3. The photographs of HTS down-converter module mounted in the vacuum chamber (upper picture) and the enclosed box that contains all parts (lower picture).

Figs. 4 and 5 are measured IF power outputs as the functions of the DC bias current, $I_B$, and the LO power level. Two maxima of the IF output against the bias current $I_B$ have been observed in all our measurements and explained in [11]. The working range of the LO power is quite broad but optimal point is around -50 dBm (include ~ 5 dB cable loss). The required LO power is very small, which is one of the advantages of the Josephson mixer.

Fig. 6 shows the measured dynamic range and linearity of the IF output power for the MMIC Josephson down-converter. A highly linear relationship of the IF output versus the RF input power is displayed with a dynamic range over 40 dB. The conversion efficiency estimated from $P_{IF}/P_{RF}$ less cable loss is approximately -11 dB, which is close to that (~ -10 dB at 55 K) subsequently measured on the large two-stage PTC.

Fig. 4. IF output versus the bias current $I_B$ at fixed RF frequency and LO power.

Fig 5. IF output as function of the LO power level at fixed frequency and RF power level.

Fig. 6. IF output power versus input RF power for the MMIC down-converter.
V. CONCLUSIONS

In this work, a portable HTS MMIC Josephson frequency down-converter operating on a commercial mini cryocooler is demonstrated. A compact unit was designed and used to house all cryocooler components, sample chamber and the MMIC HTS circuit. The HTS Josephson junction-based microwave circuit was operated successfully in the mini cryocooler unshielded and without observable performance degradation. This work demonstrated the potential of a portable all-HTS receiver front-end for application in wireless communications.

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REFERENCES