A Practical Circuit Model of High Frequency Transformers in Power Electronic Systems

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Abstract—This paper presents a practical circuit model of high frequency transformers in power electronic systems. All types of core losses, i.e., the hysteresis, classical eddy current and anomalous losses, are included in the model, and the thermal effect on magnetic hysteresis, the skin effect of eddy currents in magnetic cores, and the effect of stray capacitances are also considered. This model can therefore accurately predict the performance and core losses of transformers used in high frequency switching circuits. The practical methods for determining the circuit parameters are developed and presented in the paper, providing crucial benefit for practical applications. The developed model has been applied to simulate the performance of a 500 W transformer in a full bridge inverter operated with square waveform voltage excitation of 25 kHz. The simulations are validated by the experimental results.

Keywords—High frequency transformer, power electronic system, practical circuit model, core losses, thermal effect, stray capacitance, magnetic hysteresis, simulation.

Symbol and acronym	Definition
<i>B</i> (T)	Magnetic flux density
$C_{o}\left(\mathrm{F} ight)$	Equivalent self-capacitance
C_{pso} (F)	Equivalent mutual capacitance between primary and secondary windings
$f(\mathrm{Hz})$	Excitation frequency
$ \Phi \left({ m Wb} ight) $	Magnetic flux
H(A/m)	Magnetic field strength
HFHPD	High frequency high power density
<i>i</i> (A)	Current
L_{dl} (H)	Differential leakage inductance
$\mu_0 (\mathrm{Tm/A})$	Magnetic permeability of vacuum
μ_r	Relative magnetic permeability
Ν	Number of turns of winding
ω (rad/s)	Angular frequency
<i>P</i> (W)	Power loss
р	Subscript: primary side of transformer
$R\left(\Omega ight)$	Resistance
S	Subscript: secondary side of transformer
$\sigma(\Omega^{-1}\mathrm{m}^{-1})$	Electrical conductivity
TLM	Transmission line model
$V\left(\mathbf{V} ight)$	Voltage

I. INTRODUCTION

With the rapid development of power electronic technologies, the transformers used in the power electronic systems are operated at higher and higher frequencies to increase the power density, resulting in smaller physical size and weight (Van Wyk, 1999). As the operating frequency increases, the modeling of transformers becomes a difficult task, because a number of factors, which have been neglected previously, must be taken into account. These factors, such as the thermal effect on magnetic hysteresis of magnetic core materials, skin effects in magnetic cores, core losses including the anomalous loss, and stray capacitances, have significant influence on the performance of transformers in high frequency operations. It would be very useful for design engineers to have a simple but accurate transformer model taking all these factors into consideration. The practical model should be simple to use or easy to formulate based on the data supplied from the manufacturers and/or obtained by simple measurements, and able to predict the performance with a satisfactory accuracy.

In modern power electronic systems, soft ferrites are commonly used as the core material of electromagnetic devices such as inductors and transformers for high frequency and high power density applications because of their advantageous characteristics of high magnetic permeability and low eddy current loss at high excitation frequencies. The magnetic properties of soft ferrites, however, are sensitive to temperature (SIEMENS, 1990; Philips, 1996). This causes difficulties in performance prediction and design of electromagnetic devices. Based on sophisticated experimental study, the authors of this paper previously developed two models for considering the thermal effect on hysteresis of soft ferrites based on a careful experimental study on magnetic hysteresis loops of various soft ferrite samples from different manufacturers (Lu, 1999; Zhu, 1997). The normal scalar Preisach model, which has been proven effective and convenient

for modeling the magnetization process in magnetic cores (Hui, 1994; Zhu, 1996; Hui, 1996), has been modified to incorporate the thermal effect on magnetic hysteresis. With the modified Preisach model, it is possible to predict accurately the instantaneous B-H operating point in an excited magnetic core at any given temperature within the normal working temperature range.

At high frequencies, the effect of stray capacitances becomes significant and should be taken into account in the modeling and simulation of high frequency transformers (Blache, 1994; Laveuve, 1991; Baccigalupi, 1993; Poulichet, 2003; Massarini, 1996; Mao, 2002; Grandi, 1996). A study on experimental determination of stray capacitance of high frequency transformers by the authors (Lu, 2003) has revealed that the overall effect of stray capacitances in a two-winding transformer can be modeled properly by a π -shaped network of three capacitances or approximately as a single lumped capacitance across the primary terminals, and the capacitances can be easily determined by using the proposed experimental techniques.

This paper presents a practical but accurate dynamic circuit model of high frequency high power density (HFHPD) transformers, which takes all the factors mentioned above into account. Section 2 illustrates the circuit model of transformers as well as the discrete circuit equations obtained by using the transmission line model (TLM) based discrete technique (Hui, 1992; Zhong, 1993). Section 3 introduces the determination of circuit parameters in the model, and Section 4 shows the experimental verification of the transformer model by using a 500 W transformer supplied by a 25 kHz square waveform voltage in a full bridge inverter. Section 5 demonstrates the effects of magnetic hysteresis and stray capacitances. Finally, major conclusions and contributions of the paper are summarized in Section 6.

II. A PRACTICAL CIRCUIT MODEL OF HIGH FREQUENCY TRANSFORMERS

A two-winding high frequency transformer including the stray capacitance effect is illustrated in Fig. 1, where *R* is the winding resistance, *V* the terminal voltage, *i* the winding current, *N* the number of turns of winding, the subscripts *p* and *s* denote the primary and secondary sides, C_{po} and C_{so} are the equivalent self-capacitances of the primary and secondary windings due to the turn-to-turn, layer-to-layer and turn-to-core electrical couplings, respectively, and C_{pso} is the equivalent mutual capacitance due to the electrical coupling between the two windings.



Fig. 1. A two-winding high frequency transformer

Incorporating the *n*-stage ladder network model for magnetic cores with eddy currents (Zhu, 1996; Hui, 1996), and referring all the parameters to the primary side (normalized to one turn for simplicity of analysis), we obtain an equivalent electric circuit shown in Fig. 2, where L_{dlp} and L_{dls} are the differential leakage inductances of the primary and secondary windings, respectively, C_p , C_s and C_{ps} the equivalent capacitances of C_{po} , and C_{so} and C_{pso} referred to the normalized primary side by

$$C_p = C_{po} + (1 - k)C_{pso} \tag{1}$$

$$C_{s} = k^{2}C_{so} + k(k-1)C_{pso}$$
(2)

$$C_{ps} = kC_{pso} \tag{3}$$

and $k=N_s/N_p$.



Fig. 2. Equivalent electric circuit model of a high frequency transformer (referred to primary side normalized to one turn) In the *n*-stage ladder network, each *RL* unit represents an assumed equivalent eddy current path (to be detailed in Section 3), and i_k , R_k and Φ_k indicate the lumped eddy current, equivalent resistance for the eddy current loss in the path, and the magnetic flux linking the *k*-th equivalent eddy current path, respectively, i'_k is the mesh current for the *k*-th stage, and $d\Phi_k/di'_k$ (i.e. L_{dk}) gives the differential inductance (k = 1, 2, ..., n).

In terms of the mesh currents, the circuit equations of the ladder network can be written as:

$$R_{I}i_{I} + L_{d1}\frac{di_{1}}{dt} = 0$$
(4)

$$R_{2}i_{2} + L_{d1}\frac{d\dot{l_{1}}}{dt} + L_{d2}\frac{d\dot{l_{2}}}{dt} = 0$$
(5)

•••

$$R_{n}i_{n} + L_{d1}\frac{di_{1}}{dt} + L_{d2}\frac{di_{2}}{dt} + \bullet \bullet \bullet + L_{dn}\frac{di_{n}}{dt} = 0$$
(6)

$$\frac{V_{p}}{N_{p}} = i_{p} \frac{R_{p1}}{N_{p}} + \frac{1}{N_{p}C_{p}} \int i_{cp} dt$$
(7)

$$\frac{V_p}{N_p} - R_n \dot{i}_n = \dot{i}_p \frac{R_{p1}}{N_p} + \dot{i}_{Lp} \frac{R_{p2}}{N_p^2} + L_{dLp} \frac{d\dot{i}_{Lp}}{dt}$$
(8)

$$L_{dLs} \frac{di_{Ls}}{dt} + i_{Ls} \frac{R_{s2}}{N_s^2} + i_s \frac{R_{s1}}{N_s} + \frac{V_s}{N_s} + R_n i_n = 0$$
(9)

$$\frac{1}{N_p C_{ps}} \int i_{cps} dt = i_{Lp} \frac{R_{p2}}{N_p^2} + L_{dLp} \frac{di_{Lp}}{dt} + L_{dLs} \frac{di_{Ls}}{dt} + i_{Ls} \frac{R_{s2}}{N_s^2}$$
(10)

$$\dot{i_n} + \dot{i_{Ls}} = \dot{i_{Lp}} + \dot{i_n}$$
 (11)

$$N_{p}i_{p} = i_{Lp} + N_{p}i_{cp} + N_{p}i_{cps}$$
(12)

$$N_{p}i_{cps} + i_{Ls} = N_{s}i_{s} + N_{p}i_{cs}$$
(13)

and
$$\vec{i_k} = \vec{i_k} + \vec{i_{k+1}},$$
 $(k = 1, 2, ..., n-1)$ (14)

where R_p and R_s have been split into two parts as $R_{p1} = R_{p2} = 0.5R_p$ and $R_{s1} = R_{s2} = 0.5R_s$, respectively, for avoiding pure capacitor-inductor branches (Grossner, 1967).

By using the TLM-based discrete technique (Hui, 1992; Zhong, 1993), the differential and integral terms in the above equations can be expressed as

$$\frac{di_L}{dt} = Z_u i_L + 2V_u^{i_L} \tag{15}$$

and
$$\int i_c dt = Z_c i_c + 2V_c^{i_c}$$
(16)

where $Z_u=2/T_p$ and $Z_c=T_p/2$ are the characteristic impedances of the TLM stubs which represent an inductance of 1 H and a capacitance of 1 F, respectively, T_p is the propagation time which is equivalent to the time step in numerical simulation, and $V_u^{i_L}$ and $V_c^{i_c}$ are the incident pulses used in the TLM technique. Equations (4)-(14) can then be re-written as a set of linear equations in terms of mesh currents.

The leakage inductances in a high frequency transformer are small compared to the magnetization inductances in general. For example, a 500 W EE-core transformer (N_p =10, N_s =8) operated under 25 kHz square waveform excitation has a leakage inductance of 1.02 µH for each winding (including the test leads) referred to the primary side while the magnetization inductance is about 1.2 mH. The voltage drop across the leakage inductance is insignificant and hence the overall effect of three stray capacitors can be simplified by an equivalent stray capacitor connected on the primary side as shown in Fig. 3. The single capacitance can be approximately related to the stray capacitances in Fig. 2, by

$$C_{str} \approx C_p + C_s \tag{17}$$



Fig. 3. Practical circuit model of high frequency transformer with single equivalent stray capacitance

III. PARAMETER IDENTIFICATION METHOD

3.1 Equivalent eddy current paths

Due to the skin effect generated by the eddy currents in the magnetic core at a high switching frequency, the distribution of magnetic flux density is not uniform over the cross section of the magnetic core. In order to account for the skin effect, the magnetic core should be subdivided into a number of small parts with specified properties (Zhu, 1996; Hui, 1996). Firstly, the magnetic core can be subdivided into several (say q)

segments along the main magnetic flux paths such that each segment has a unique cross-sectional area. This can be done based on the physical dimensions of the magnetic core. Secondly, the cross section of each segment can be subdivided into a number of (say *n*) eddy current paths such that the distribution of magnetic flux density can be assumed to be uniform within each path. All segments should have the same number of eddy current paths. To determine the number of the paths, *n*, the maximum width of the paths, w_s , should be less or equal to the skin depth δ at the fundamental excitation frequency *f* by

$$w_s \le \delta = \sqrt{\frac{2}{\omega \sigma \mu_r \mu_0}} \tag{18}$$

where $\omega = 2\pi f$ is the angular frequency of the excitation, σ the electrical conductivity of the material, μ_0 the magnetic permeability in free space, and μ_r the relative permeability of the core material. The *k*-th equivalent eddy current path of the entire magnetic core is assumed to be the combination of all the *k*-th eddy current paths in all the core segments and comply with the principle of magnetic flux continuity, (k = 1, 2, ..., n).

3.2 Equivalent eddy current loss resistance

The assumed eddy current paths over the rectangular cross section of one segment of the EE core are shown in Fig. 4, in which all the eddy current paths have the same width. Since the eddy current density vector can be assumed one-dimensional at every point over the cross section, each eddy current path can be transferred into a pair of slices shown in Fig. 5. The length of each slice (a_k) , is the half of the mean length of the assumed eddy current path in Fig. 4 while the width (Δy) keeps unchanged. The total core loss in the *k*-th (k=1,2,...n) pair of slices is equivalent to the one occurred in the corresponding eddy current path of Fig. 4. Therefore, based on the study on ring cores (Zhu, 1996; Hui, 1996), the total classical eddy current and anomalous loss in the *k*-th eddy current path of the EE core can be calculated by (Lu, 1998)

$$P_{eak} = \sum_{i}^{q} \left(\frac{2a_{ik}l_i \Delta y_i}{\sigma_i} \right) J_{ik}^{2} r_{Jik} r_{Cik}$$
⁽¹⁹⁾

where l_i is the segment length along the direction of the main flux density B_z , J_{ik} the eddy current density at $y=y_k$, $r_{JiK}=[1+(1+J_{i(k-1)}/J_{ik})J_{i(k-1)}/J_{ik}]/3$ a factor considering the variation of current density in the *k*-th path, $r_{cik}=1+C_a/(C_e\sqrt{|dB_z/dt|_{ik}})$ a correction factor accounting for the anomalous loss in the *k*-th path of the *i*-th segment, and C_e and C_a are the eddy current and anomalous loss coefficients, respectively. C_a and C_e can be determined by the core loss separation procedure described in (Zhu, 1996; Hui, 1996).

In the equivalent circuit shown in Fig. 2 or 3, the eddy current loss resistance R_k (k=1,2,...,n) accounts for the total eddy current loss in the *k*-*th* assumed eddy current path of all segments and can be calculated by

$$R_k = \frac{V_k^2}{P_{eak}} \tag{20}$$

where V_k is the voltage across the resistance R_k .



Fig. 4. Rectangular cross section with assumed eddy current paths



Fig. 5. Equivalent pair of slices of the k-th eddy current path in Fig. 4

3.3 Equivalent differential inductances

At a time instant, the mesh currents $i_k'(k=1,2,..,n)$ can be obtained by solving simultaneously (4)-(14) or the equivalent linear equations deduced from these equations by the TLM technique (Lu, 1998). The magnetic flux density and magnetic field strength in different eddy current paths of a segment are obtained by solving the non-magnetic circuit using the Newton-Raphson method. When the flux densities of two consecutive time steps are obtained, the differential inductance corresponding to the *k*-th path can be calculated by

$$L_{dk} = \frac{d\Phi_k}{d\dot{l_k}} \cong A_k \frac{B_{k(m)} - B_{(m-1)}}{\dot{l_k(m)} - \dot{l_k(m-1)}}$$
(21)

where A_k is the cross-sectional area of the *k*-th eddy current path in the segment with the excitation coil, B_k the flux density, and subscripts (*m*) and (*m*-1) refer to the *m*-th and (*m*-1)-th time steps, respectively.

3.4 Lumped stray capacitances

The lumped stray capacitances in the circuit shown in Fig. 2 can be determined by using the experimental techniques proposed in (Lu, 2003).

IV. Experimental Verification

4.1 Test transformer

A 500 W EE soft ferrite (N27) core two-winding transformer provided by ex-EXCOM, Australia, was chosen as the test transformer. It was designed for a full bridge inverter in a telecommunication system and with 25 kHz square waveform voltage excitation. The materials and parameters of the major components of the test transformer are listed in Table 1.

Table I

Parameters of a 500 W 25 kHz EE core transformer

Core material	Soft ferrite N27 (Siemens)		
Winding material	Copper foil of 0.10 mm thickness		
Number of turns	Primary winding: Secondary winding:	10 turns 8 turns	
Winding resistance (DC)	Primary winding: Secondary winding:	0.014 Ω 0.012 Ω	
Insulation material	NOMEX T410, thickness: 80 µm, and width: 44 mm		

4.2 Simulation and experimental verification

Because of the very low conductivity of the soft ferrite used in the test transformer, the skin depth is quite large at 25 kHz, and a two stage ladder network would be sufficiently accurate for simulating the non-uniform eddy currents in the core.

4.2.1 No-load test with square wave primary voltage excitation

Under the no-load condition or the secondary side open circuited, the waveforms of the primary current and the secondary voltage of the test transformer with respect to a 25 kHz square wave primary voltage excitation were simulated by using the circuit models shown in Figs. 2 and 3, respectively, and compared with experimental measurement under the same conditions. The three lumped stray capacitances used in Fig.2 are C_p =2.65 nF, C_s =0.82 nF, and C_{ps} =0.17 nF (referred to one turn), which were determined by the two-port network approach (Lu, 2003), and the single lumped stray capacitance in Fig.3 is C_{str} =0.37 nF (referred to one turn), which was determined by the step-response approach (Lu, 2003). The operational temperature is 62 °C. The normal time step for the TLM based discrete solution was set as 39.06 ns (1024 step in one cycle) and the sub-time step was set as one tenth of the normal time step.

The predicted voltage and current waveforms and the flux against the excitation current loops obtained by using the two circuits are plotted against the experimental results in Figs. 6-7, respectively. It is shown that the simulated results, which are obtained by using both the three lumped stray capacitances and the single equivalent capacitance, agree substantially with the experimental results. The noticeable discrepancies around the beginning and the end of a switching period might be due to the error introduced by the interpolation.

Tables II and III tabulate the measured and calculated core losses under the no-load condition using the equivalent circuits in Figs. 2 and 3, respectively. The percentage errors of core loss prediction are less than 5%, and the model with three stray capacitance appears to be slightly more accurate.

Table II

Comparison of the calculated and measured core losses under no-load condition using the circuit in Fig. 2

Excitation Voltage	P_{core} (cal.) (W)	P_{core} (exp.) (W)	Error (%)
Square wave, 150V (25kHz)	21.35	20.41	4.59

Table III

Comparison of the calculated and measured core losses under no-load condition using the circuit in Fig. 3

Excitation Voltage	P_{core} (cal.) (W)	P _{core} (exp.) (W)	Error (%)
Square wave, 150V (25kHz)	21.24	20.41	4.06



(a)



Fig. 6. Comparison of simulated and measured results at 25 kHz, no-load, using three stray capacitor model in Fig.2 with C_p =2.65 nF, C_s =0.82 nF, and C_{ps} =0.17 nF: (a) primary current and secondary voltage, and (b) flux against excitation current





(b)

Fig. 7. Comparison of simulated and measured results at 25 kHz, no-load, using single stray capacitor model in Fig.3 with $C_{str}=3.70$ nF: (a) primary current and secondary voltage, and (b) flux against excitation current

4.2.2 Load tests with square wave primary voltage excitation

The waveforms of the primary current and the secondary voltage and current of the test transformer under a 150 V 25 kHz square wave primary voltage excitation were simulated by using the circuit model in Fig. 3 with C_{str} =0.37 nF, under two different loads (R =10.52 Ω , L = 13.98 μ H; and R =4.96 Ω , L = 9.10 μ H), with the operating temperatures of 70 °C and 81 °C, respectively. The normal time step for the TLM based discrete solution was set as 39.06 ns and the sub-time step was set as one hundredth of the normal time step.

The voltage and current waveforms predicted by using the two circuit models are plotted against the experimental results in Figs. 8 and 9, respectively. It is shown that the simulated results, which are obtained

by using both the three lumped stray capacitances and the single equivalent stray capacitance, agree substantially with the experimental results.



(a)



(b)

Fig. 8. Comparison of measured and simulated results by using single stray capacitor model in Fig.3 with C_{str} =3.70 nF under an inductive load of R =10.52 Ω and L = 13.98 μ H at 25 kHz: (a) primary current, (b) secondary current and voltage





(b)

Fig. 9. Comparison of measured and simulated results by using single stray capacitor model in Fig.3 with C_{str} =3.70 nF under an inductive load of R=4.96 Ω and L=9.10 μ H at 25 kHz: (a) primary current, (b) secondary current and voltage

V. EFFECTS OF STRAY CAPACITANCE AND MAGNETIC HYSTERESIS

5.1 Effects of stray capacitance

In the measured waveform of the primary current at no-load shown in Fig.6(a), there is a big dip at the instant of switching, resulting in two large reversals in the flux-excitation current loop shown in Fig.6(b), and the size of this dip decreases when the transformer is loaded as shown in Figs. 8(a) and 9(a). This is attributed to the effect of the large stray capacitances because of the foil windings used in the test transformer. Fig. 10 plots the simulated waveform of the primary current when the stray capacitances are

ignored in the simulation model against the measured waveform. It can be seen that the stray capacitances can cause significant deformation in the primary current. However, the effect of stray capacitances has little influence on the accuracy of core loss prediction since the stray capacitors can be regarded as lossless, as shown in Fig. 11 in which the comparison of the measured and simulated flux against the excitation current loops is illustrated. It might be acceptable to ignore the stray capacitances in the proposed model when only core loss prediction is a main focus.



Fig. 10. Comparison of measured and simulated waveforms of primary current ignoring stray capacitances



Fig. 11. Comparison of measured and simulated loops of mutual fluxes vs. excitation current ignoring stray capacitances

5.2 Effects of hysteresis

The hysteresis of magnetic cores has a significant influence on transformer performance. Fig. 12 plots the no load primary current predicted when the normal magnetization curve instead of the hysteresis model was used in the simulation together against the experimental results. It is shown that neglecting the hysteresis in the model of magnetic cores can cause a considerable discrepancy between the simulated and experimental results.



Fig. 12. Comparison of measured and simulated waveforms of primary current when hysteresis is not considered in the model

VI. CONCLUSIONS

In conclusion, a practical circuit model and its simplified version for HFHPD transformers have been developed and confirmed by experimental measurements. The main magnetic flux in a HFHPD transformer can be modeled by the dynamic circuit of general magnetic cores. The skin effect of eddy currents in magnetic cores can be accounted for by a ladder network of non-linear resistances and non-ideal inductances. A modified scalar Preisach model of hysteresis incorporating the thermal effects is used for tracing the magnetization history and calculating the hysteresis loss. The values of leakage inductances can be determined conveniently by experimental measurement, i.e. the short circuit impedance method. The stray capacitances of HFHPD transformers can be modeled properly by a π -shaped network of three capacitors or approximately by a single capacitor across the primary terminals. The comparison between the

simulated and measured results shows that the developed practical circuit model can truthfully predict the performance of HFHPD transformers under no-load and different load conditions. The percentage error of core loss prediction is less than 5%. The ignorance of magnetic hysteresis and effects of stray capacitances could cause significant discrepancies between the simulated and experimental results.

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